

Customer No.: 31561
Application No.: 10/709,953
Docket No.: 10546-US-PA

In The Claims:

1. (currently amended) A chip packaging structure, comprising:
a chip having a first passivation layer and at least a bonding pad, wherein the bonding pad is exposed by the first passivation layer and the first passivation layer has at least a recess;
a redistribution layer formed over the first passivation layer, wherein the redistribution layer electrically connects with the bonding pad and extends from the bonding pad to the recess;
a second passivation layer formed over the first passivation layer and the redistribution layer, wherein the second passivation layer has an opening that exposes the redistribution layer above the recess; and
at least a bump disposed inside the opening and electrically connected to the redistribution layer above the recess.
2. (currently amended) The chip packaging structure of claim 1, wherein an obtuse angle is formed between a sidewall of the recess and a bottom surface of the recess.
3. (currently amended) The chip packaging structure of claim 1, further comprising at least an under-bump-metallurgy layer between the redistribution layer that is exposed by the opening and the bump.
4. (withdrawn) The chip structure of claim 3, wherein the under-bump-metallurgy layer further comprises:
a first metallic layer formed over the opening-exposed redistribution layer; and
a second metallic layer formed over the first metallic layer.
5. (withdrawn) The chip structure of claim 4, wherein a material constituting the first

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metallic layer is selected from the group consisting of aluminum, titanium, titanium-tungsten alloy, tantalum, tantalum nitride and chromium.

6. (withdrawn) The chip structure of claim 4, wherein a material constituting the second metallic layer comprises copper.

7. (withdrawn) The chip structure of claim 4, wherein the under-bump-metallurgy layer further comprises at least an electroplated layer formed over the second metallic layer and the electroplated layer is selected from the group consisting of an electroplated copper layer, an electroplated nickel layer, an electroless nickel layer, an electroless plated gold layer and combination thereof.

8. (currently amended) The chip packaging structure of claim 3, wherein the under-bump-metallurgy layer further comprises:

- a first metallic layer formed over the opening-exposed redistribution layer;
- a second metallic layer formed over the first metallic layer; and
- a third metallic layer formed over the second metallic layer.

9. (currently amended) The chip packaging structure of claim 8, wherein a material constituting the first metallic layer is selected from the group consisting of aluminum, titanium, titanium-tungsten alloy, tantalum, tantalum nitride and chromium.

10. (currently amended) The chip packaging structure of claim 8, wherein a material constituting the second metallic layer is selected from the group consisting of nickel-vanadium alloy and copper-chromium alloy.

11. (currently amended) The chip packaging structure of claim 8, wherein a material

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constituting the third metallic layer comprises copper.

12. (currently amended) The chip packaging structure of claim 8, wherein the under-bump-metallurgy layer further comprises at least an electroplated layer formed over the third metallic layer and the electroplated layer is selected from the group consisting of an electroplated copper layer, an electroplated nickel layer, an electroplated gold layer and combination thereof.

13. (withdrawn) The chip packaging structure of claim 1, wherein the redistribution layer further comprises:

- a first metallic layer formed over the first passivation layer; and
- a second metallic layer formed over the first metallic layer.

14. (withdrawn) The chip packaging structure of claim 13, wherein a material constituting the first metallic layer is selected from the group consisting of aluminum, titanium, titanium-tungsten alloy, tantalum, tantalum nitride and chromium.

15. (withdrawn) The chip packaging structure of claim 13, wherein a material constituting the second metallic layer comprises copper.

16. (currently amended) The chip packaging structure of claim 1, wherein the redistribution layer further comprises:

- a first metallic layer formed over the first passivation layer;
- a second metallic layer formed over the first metallic layer; and
- a third metallic layer formed over the second metallic layer.

17. (currently amended) The chip packaging structure of claim 16, wherein a material

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constituting the first metallic layer is selected from the group consisting of aluminum, titanium, titanium-tungsten alloy, tantalum, tantalum nitride and chromium.

18. (currently amended) The chip packaging structure of claim 16, wherein a material constituting the second metallic layer is selected from the group consisting of nickel-vanadium alloy and copper-chromium alloy.

19. (currently amended) The chip packaging structure of claim 16, wherein a material constituting the third metallic layer comprises copper.

20. (currently amended) The chip packaging structure of claim 1, wherein an obtuse angle is formed between a sidewall of the opening and a bottom surface of the opening.